

FORMHOR 49 (Based on Form PTO-1449)

PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Sheet 1 of 2

			
ATTORNEY DOCKET No.	SERIAL NO.		
MP0020 09/737,743			
APPLICANT			
Sutardja, Sehat			
FILING DATE	GROUP		
December 18, 2000	2631		

U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	PP	4,112,253	9/1978	Wilhelm		
2.		4,152,541	5/1979	Yuen		
3.		4,393,370	7/1993	Hareyama		
4.		5,388,092	2/1995	Koyama et al		
5.		5,666,354	9/1997	Cecchi et al		
6.		5,796,725	8/1998	Muraoka		
7.		5,822,426	10/1998	Rasmus et al		
8.		5,825,819	10/1998	Cogburn		
9.		5,864,587	1/1999	Hunt		
10.		6,201,841		Iwamatsu et al		
11.		6,744,931		Komiya et al		
12.		6,468,032 B1	6/2002	Lye et al		
13.		6,576,746 B2		McBride et al		
14.		4,535,206	8/1985	Falconer		
15.		4,878,244	10/1989	Gawargy		
16.		RE 30,111	10/1979	Blood, Jr.		
17.		5,887,059	3/1999	Xie et al		
18.	√	6,577,114	6/10/2003	Roo, Pierte		

Examiner:	Phung phy	Date Considered:	11/25/06	



FOR HDP-1449 (Based on Form PTO-1449)

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December 18, 2000	2631

FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document . Number	Date	Country	Class/ Subclass	Translatio Yes	on No
1.	rr	WO 99/46867	9/1999	Agazzi et al			
2.		WO 00/28663 A2	5/2000	Hatamian et al			
3.		WO 00/28663 A3	5/2000	Hatamian et al			
4.		WO 00/28691 A2	5/2000	Agazzi et al			
5.		WO 00/28691 A3	5/2000	Agazzi et al			

OTHE	OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)						
Ref. Desig.	Examiner's Initials						
1.	PP	Sailesh Rao; Short Course: Local Area Networks, International Solid State Circuits Conference; Sailesh Rao; Outline Implementing Gigabit Ethernet Over Cat-5 Twisted-Pair Cabling; Jack Kenny; Signal Processing and Detection in Gigabit Ethernet; 3 pages					
2.		Kamran Azadet and Chris Nicole; Low-Power Equalizer Architectures for High-Speed Modems; October 1998; pages 118-126					
3.		Techdictionary.com definition of decoder, Link: http://www.teghdictionary.com; 1 page					
4.	V	University of Pennsylvania CSE Digital Logic Lab re decoders. Link: http://www.cse,dmu.ac.uk/~sexton/WWW/Pages/cs2.html; 3 pages					

Examiner:	Phumphu	Date Considered:	1/25/00	6	

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U.S. PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date	
1.	PS	6,566,677	4/2003	Hardy			
2.		5,878,340	4/1999	Chatterjee et al			
3.	V	5,202,528	4/1993	lwaooji			

FORE	FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No	
1.								

OTHE	OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)					
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Examiner:

phung phu

Date Considered:

1/25/06